A10B25M

10-bit, 25MSPS Analog-to-Digital Converter IP block

General Description

The A10B25M is an ultra low-power, high-performance analog to digital converter (ADC) intellectual property (IP) design block. It is a pipeline ADC that has 10-bit resolution and a sampling rate of up to 25 megasamples per second (MSPS).

The A10B25M maintains its high-performance while consuming an exceptionally low power of only 6 mW, making it an outstanding solution for high efficiency designs and applications.

The cost-effective IP block has been designed and verified in a 180 nm CMOS process.

The ADC IP is also available in a radiation-tolerant version, that can function under harsh environmental constraints.

Key features

- 10 bit resolution
- 25 MSPS sampling rate
- 6 mW power
- 25 MHz Input Bandwidth
- Dynamic Performance:
  - SFDR: 71 dBc
  - ENOB: 8.5
- Hard IP block
- TowerJazz 180 nm process
- Silicon-Validated
- Radiation-tolerant design available: A10B25MRH
- Available as IP and Integrated Circuit

Applications

- Wireless and Wired Communications
- Sensor/Detector Readout
- Imaging applications
  - Image Sensor Readout
  - Infrared FPA Readout
  - Medical Imaging applications
- Automotive applications
- Military & Civil Aerospace

To get more information

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